Sieve of Eratosthenes CS 450

Peter Chinetti School of Electrical and Computer Engineering Illinois Institute of Technology Chicago, Illinois 60616 Email: peter@chinetti.me A20265405

Abstract—Results and analysis of runtime of the sieve of Eratosthenes on machines with 4, 8 and 32 cores.

I. INTRODUCTION

Although the problem only called for testing on Alpha, it was decided that comparing results across machines with different numbers of cores would be interesting. The three machines choosen for the comparison were a workstation with an Intel i5 2500k processor, a server with two Intel Xeon E5410 processors, and Alpha with two AMD Opteron 6272 processors.

II. RESULTS ON THE 15

The graph detailing the results for the i5 can be found in figure 1. In figure 1, and all figures, the horizontal axis is pool size and the vertical axis is chunk size.

III. RESULTS ON THE XEON

Results can be found in Figure 2.

IV. RESULTS ON THE OPTERON

Results can be found in Figure 3.

V. DISCUSSION OF RESULTS

Generally across all machines, perfomance increased as chunk size increased. This can be explained by larger chunk size reducing the time spent switching between chunks. Increasing poolsize helped performance until there were more processes in the pool than there were on the processor. Adding processes after "saturating" the processor only added switching overhead and slowed down processing.



Fig. 1. Results on the 4 core i5



Fig. 2. Results on the 8 core Xeon



Fig. 3. Results on the 32 core Xeon